AMENDMENT TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF CLAIMS:

Claims 1 to 11. (Canceled).

12. (Currently Amended) A system for reconfiguring a programmable unit, the programmable unit including a plurality of reconfigurable function cells in a multidimensional arrangement, comprising:

a primary logic unit in communication with at least one of the plurality of reconfigurable function cells, the primary logic unit configured to detect an event and to detect a state of the at least one of the plurality of reconfigurable function cells;

a first memory configured to store a first configuration data associated with a selected one of the plurality of reconfigurable function cells;

a jump table coupled to the primary logic unit having a plurality of entries, at least one of the plurality of entries configured to store a memory address of the first configuration data; and

a FIFO memory shared by the plurality of reconfigurable function cells and coupled to the primary logic unit configured to store a plurality of configuration data associated with the plurality of reconfigurable function cells, the plurality of configuration data including the first configuration data.

13. (Previously Presented) The system according to claim 12, wherein, when the primary logic unit detects the event, the primary logic unit reads the FIFO memory to determine whether a configuration data from the plurality of configuration data is stored in the FIFO memory.

14. (Currently Amended) The system according to claim 13, wherein, when the primary logic unit determines that no configuration data is stored in the FIFO memory, the primary logic unit retrieves the memory address, retrieves the first configuration data from the first memory based on the memory address, and one of reconfigures the selected one of the plurality of reconfigurable function cells based on the configuration